

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S47	92	(vliw (very adj long adj instruction adj word)) with (convert\$4 transform\$4 modif\$4) with (processor multiprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:19
S49	5	(vliw (very adj long adj instruction adj word)) same (superscalar pipelin\$4) and (binary adj compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:23
S50	6	(vliw (very adj long adj instruction adj word)) and (superscalar pipelin\$4) and (binary adj compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:23
S51	7	(vliw (very adj long adj instruction adj word)) and (binary adj compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:34
S52	24	((isa) (instruction adj set adj architecture)) and (binary adj compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:29
S54	3	(vliw (very adj long adj instruction adj word)) and ((isa) (instruction adj set adj architecture)) and (binary adj compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 11:36
S56	17	(pipelin\$4 adj scalar) and ((isa) (instruction adj set adj architecture))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 15:31
S59	20	((non adj operational) (nop) (no adj operation)) and ((isa) (instruction adj set adj architecture)) and (binary adj compatibl\$4) ((binary adj incompatibl\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 15:25

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S60	3	((non adj operational) (nop) (no adj operation)) and ((isa) (instruction adj set adj architecture)) and ((binary adj compatibl\$4) (binary adj incompatibl\$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 15:25
S61	175	partial near4 execut\$4 and ((isa) (instruction adj set adj architecture))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 15:32
S63	60	partial near4 execut\$4 and ((isa) (instruction adj set adj architecture)) and (vliw (very adj long adj instruction adj word))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 16:03
S64	342	dsp near6 vliw	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 16:03
S66	1	dsp near6 vliw and (binary near4 compatibl\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 16:04
S67	75	dsp near6 vliw and ((isa) (instruction adj set adj architecture))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/07/30 16:13